

ABSTRACT OF THE DISCLOSURE

There is provided a liquid crystal display drive method that uses a drive voltage waveform consisting of a display signal period (display waveform 32 bits) and a control signal period irrelevant to display (control waveform 2 bits) in a given time or a period of plural frames or one frame. This method suppresses generation of internal DC voltage and thus prevents impurity ions from deteriorating the quality of displayed pictures.

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